


IFW

B

TRANSMITTAL OF FORMAL DRAWINGS				Docket No. YOR920010499US1 (14710)	
In Re Application Of: Stephen W. Bedell, et al.					
Serial No. 10/055,138	Filing Date January 23, 2002	Confirmation No. 9190	Examiner Stephen J. Stein	Art Unit 1775	
Invention: METHOD OF CREATING HIGH-QUALITY RELAXED SiGe-ON-INSULATOR FOR STRAINED Si CMOS APPLICATION					
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450					
Transmitted herewith are: 4 sheets of formal drawing(s) for this application. <input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).					
<div style="text-align: center;"> _____ <i>Signature</i></div> <div style="border: 1px solid black; padding: 5px; margin-top: 10px;">Steven Fischman Registration No. 34,594 Correspondence Address Customer No. 23389</div>			Dated: <u>April 30, 2004</u>		
			<div style="border: 1px solid black; padding: 5px;"><div style="text-align: center;">I certify that this document and attached formal drawings are being deposited on _____ with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</div><div style="text-align: center; margin-top: 10px;">_____ <i>Signature of Person Mailing Correspondence</i></div><div style="text-align: center; margin-top: 10px;">_____ <i>Typed or Printed Name of Person Mailing Correspondence</i></div></div>		